



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **HIRAOKA, Tetsuya, et al.**

Serial No.: 09/973,002

Filed: **October 10, 2001**

Group Art Unit: **2827**

Examiner: **Lourdes C. Cruz**

P.T.O. Confirmation No.: 4785

For: **SEMICONDUCTOR DEVICE WITH STACK OF SEMICONDUCTOR CHIPS**

RESPONSE UNDER 37 CFR §1.116
- EXPEDITED RESPONSE -
GROUP ART UNIT

MAILSTOP AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: May 30, 2003

Sir:

A response to the Office Action dated January 30, 2003, is extended to May 30, 2003 by a one month Petition for Extension of Time, please amend the above-identified application as follows:

IN THE DRAWINGS:

Submitted herewith is a Request for Approval of Drawing Corrections, along with proposed drawing corrections to Figures 1 and 2, as marked in red ink.

The applicants respectfully request that the proposed drawing corrections submitted herewith be approved by the Examiner, and that the outstanding objection to the drawings be withdrawn.